

PATENT  
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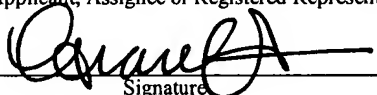
**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants : Masaru KIDOH et al.  
U.S. Serial No. : 10/786,442  
Filing Date : February 25, 2004  
For : ***SEMICONDUCTOR DEVICE HAVING BIT-LINE  
CONTACTS, AND METHOD OF MANUFACTURING THE  
SAME***  
Examiner : Edward Joseph Wojciechowicz  
Group Art Unit : 2815

745 Fifth Avenue  
New York, New York 10151

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on February 18, 2005.

Grace M. Pan, Reg. No. 39,440  
(Name of Applicant, Assignee or Registered Representative)

  
Signature

February 18, 2005  
Date of Signature

**RESPONSE TO RESTRICTION REQUIREMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

In response to the Office Action mailed January 26, 2005, setting a one-month period for reply and asserting a restriction requirement, Applicants elect Group I (claims 1-12), without traverse, for further prosecution in this application.

Applicants reserve the right to file divisional applications to non-elected subject matter.

If any issues remain, or if the Examiner has any further suggestions, he is invited to call the undersigned at the telephone number provided below.

The Examiner is hereby authorized to charge any insufficient fees or credit any overpayment associated with the above-identified application to Deposit Account No. 50-0320.

The Examiner's consideration of this matter is gratefully acknowledged.

Respectfully submitted,

FROMMER LAWRENCE & HAUG LLP  
Attorneys for Applicants

By:



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